

Patent

Docket No.: Intel 2207/10083

Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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JUL 19 2004

Technology Center 2100

APPLICANT : Talal K. JABER
SERIAL NO. : 09/751,750
FILED : December 29, 2000
FOR : MICROPROCESSOR ON-CHIP TESTING ARCHITECTURE
AND IMPLEMENTATION
GROUP ART UNIT : 2133
EXAMINER : Guy J. LAMARRE

M/S:
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class in an envelope addressed to M/S: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on

Dated: July 7, 2004

Signature:

Blanche Guzman-Salmon
Blanche Guzman-Salmon

PETITION FOR EXTENSION OF TIME

Dear Sir:

Applicant respectfully requests a three month extension of time in which to file a response to the Office Action mailed January 12, 2004 for which a response period expired on April 12, 2004. The extended period expires on July 12, 2004. The Commissioner is hereby authorized to charge payment of the 37 C.F.R. §1.136(a) additional extension fee of **\$950.00** to Deposit Account No. **11-0600**.

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The Commissioner is hereby authorized to charge payment of any additional fees and/or patent application processing fees required under 37 C.F.R. §1.16 and §1.17 or credit any overpayment to Deposit Account No. **11-0600**.

Respectfully submitted,

KENYON & KENYON

Dated: July 7, 2004

By: 

Sumit Bhattacharya
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